Specifications:

Product name	S1C17W18	
Core CPU	Epson original 16-bit RISC Processor + Multiplier/Divider	
OSC3 oscillator circuit	Crystal/ceramic oscillator 4.2 MHz (max.) CR oscillator 2.1 MHz (max.)	
OSC3 oscillator circuit (internal)	250kHz, 384kHz, 500kHz, 1MHz, 2MHz and 4MHz	
OSC1 oscillator circuit	Crystal oscillator 32.768 kHz (typical)	
Internal oscillator circuit	700 kHz	
On-chip ROM	128 kB Flash ROM (for instructions and data) 1,000 erase/program cycles On-board programming by debugging function Self programming by software control Read/program protection	
On-chip RAM	8 KB	
I/O Ports	Max. 67-bit general-purpose I/O ports (Terminals are shared with peripheral I/O ports)	
A/D converter	7 input ports + 1 port for internal temperature sensor (12-bit successive approximation)	
Serial interfaces	SPI	2 ch.
	I²C	1 ch.
	UART	2 ch.
LCD driver	Max. 352 segments; 44 SEG x 8 COM Max. 192 segments; 48 SEG x 4 COM	
Supply voltage detector (SVD)	30 programmable levels (1.2V - 3.6V)	
Operating voltage	Guaranteed operating range: 1.2V - 3.6V A/D converter operating range: 1.8V - 3.6V	
Current consumption	SLEEP mode	0.15µA (typical)
	HALT mode (32.768 kHz)	0.3µA (typical) [5]
	RUN mode (32.768 kHz)	4.0µA (typical) [5]
	RUN state (1 MHz)	150µA (typical)
R/F converter	2 input channels (CR oscillator [6] with 24-bit counters)	

Product name	S1C17W18
Packages	TQFP15-128 package (14 mm x 14mm with 128pin and a pin pitch of 0.4mm) SQFN9-64 package (9mm x 9mm with 64 pins and a pin pitch of 0.5mm) Bare chip with 80μm (min.) pad pitch

[5] In Super Economy mode

[6] An oscillator that uses capacitors and resistors to generate frequency