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Simplifies troubleshooting when using highly complex SoCs:

PLS' UDE 4.6.2 with unique search function supports the rapid analysis of very large amounts of trace data

Lauta (Germany) – June 13, 2016 – With highly complex automotive microcontrollers – such as Infineon's AURIX family or the PowerArchitecture-based SPC58E series from STMicroelectronics – very large amounts of trace data accrue in a very short period of time, especially when the data are recorded through a high bandwidth trace interface like Aurora. For example, PLS' Universal Access Device 3+ (UAD3+) is able to record up to 4 GB compressed trace with up to 500 Mbytes/s from the target through this interface.

So that the users do not lose the overview in this decoded trace stream – enriched with additional data from the debug information of the ELF files – the latest version 4.6.2 of the Universal Debug Engine (UDE) from PLS Programmierbare Logik & Systeme has now been expanded by a unique high-performance search function.

For example, with the new function 'Find all' all entries which have the same address can be found very quickly. The search results are displayed in a separate results list, from which in turn it is simple to navigate to the hits in the UDE own trace window. In addition, the trace window itself has been expanded by visualization and navigation elements, which highlights all hits with a marker. Among other things, that feature enables the user to get very quickly an overview, how often a certain instruction was actually executed. A simple navigation between the individual hits is of course also possible.

Bookmarks, which are also introduced with the new version 4.6.2 of UDE, ensure a considerable simplification when handling usually very large amounts of trace data. With these bookmarks, it is now possible for developers to mark interesting positions in the trace, and of course quickly find them again later on.

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PLS Programmierbare Logik & Systeme GmbH

PLS Programierbare Logik & Systeme GmbH, based in Lauta, Germany, was founded in 1990. Since then, with its innovative test and development tools, the company has demonstrated its position as an international technology leader in the field of debuggers, emulators and trace solutions for embedded systems. The modular and flexible software architecture of PLS's Universal Debug Engine (UDE) guarantees optimal conditions for debugging SoC-based systems. For example, with the intelligent use of modern on-chip debug and trace units, valuable functions such as profiling and code coverage are available for system optimization and test. PLS's Universal Access Device product family (UAD2/UAD3+) complete the full featured debug solution with an efficient and high-speed target access with transfer rates of up to 3.5 MBytes/s and a flexible adapter concept supporting a wide range of different target interfaces. The leading edge UDE/UAD debugging infrastructure offers entirely new dimensions for fast and flexible access to multi-core systems with the support of important architectures such as AURIX/TriCore, Power Architecture, Cortex/ARM, XC2000/XE166 as well as simulation platforms of different vendors. For further information about the company, please visit www.pls mc.com.

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