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# NI Lowers Semiconductor ATE Cost With PXI-Based Test **Systems**

Open, platform-based approach combines modular instrumentation and system design software for RF and mixed-signal production test

NI (Nasdag: NATI), the provider of solutions that enable engineers and scientists to solve the world's greatest engineering challenges, today announced the NI Semiconductor Test System (STS) series. These PXI-based automated test systems reduce test cost for RF and mixed-signal devices by opening access to NI- and industry-offered PXI modules in semiconductor production environments. Compared to conventional semiconductor automated equipment (ATE), STS lead users are experiencing reduced production costs and increased throughput and can now perform both characterization and production with the same hardware and software tools. This decreases data correlation time and time to market.

"As integrated circuit complexity grows exponentially, cost-effective ATE that provides optimal test coverage in applications from design verification to end-ofline production test is increasingly important," said Dr. Hans-Peter Kreuter, senior design and application engineer for automotive body power products at Infineon Technologies. "For mixed-signal test, the PXI-based STS outperforms what we typically see in traditional ATE with optimal test coverage at a very low cost."

The open, modular architecture of STS gives engineers access to cutting-edge PXI instrumentation, unlike traditional ATE with its closed architecture. This is particularly important for RF and mixed-signal test, as the requirements of the latest semiconductor technologies often outpace the test coverage provided by traditional ATE. Powered by TestStand test management software and LabVIEW system design software, STS comes with a rich set of features for semiconductor including a customizable production environments, operator handler/prober integration, device-centric programming with pin-channel mapping. standard test data format reporting and integrated multisite support. With these features, engineers can quickly develop, debug and deploy test programs, shortening overall time to market. Additionally with the fully enclosed "zerofootprint" test head, standard interfacing and docking mechanics, STS comes ready to integrate into a semiconductor production test cell.

"Traditional ATE systems require major costly retooling efforts on the test floor as generations of test systems become obsolete or unable to meet new test requirements, but the nature of the open PXI architecture of the STS helps us retain our original investment and build upon it, rather than throw it away," said Glen Peer, director of test for Integrated Device Technology (IDT). "It provides the flexibility we need to reconfigure and grow our test platforms in parallel with our growing performance needs."

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## **Editor Contact**

#### Eva Heigl

Marketing Communications Manager Central European Region Tel.:+49 89 741313-184 eva.heigl@ni.com

#### Stefan Ambrosch

Ad & PR Specialist Tel.: +49 89 741313-136 stefan.ambrosch@ni.com

#### Florian Schultz

Ad & PR Specialist Tel.: +49 89 741313-294 florian.schultz@ni.com

# **Reader Contact**

# Germany:

National Instruments Germany GmbH Ganghoferstraße 70 b 80339 München Tel.: +49 89 7413130 Fax: +49 89 7146035 ni.com/germany

info.germany@ni.com

#### Austria:

National Instruments GesmbH Plainbachstraße 12 5101 Salzburg-Bergheim Tel: +43 662 457990-0 Fax: +43 662 457990-19 ni.com/austria ni.austria@ni.com

#### Switzerland:

National Instruments Switzerland GmbH Sonnenbergstrasse 53 5408 Ennetbaden Tel.: +41 56 2005151

Fax: +41 56 2005155 ni.com/switzerland ni.switzerland@ni.com



The STS series includes three different models named T1, T2 and T4, which accommodate one, two and four PXI chassis, respectively. These varying sizes, along with common software, instrumentation and interconnect mechanics across all STS models give engineers the ability to optimize for a wide range of pin-count and site-count requirements. Additionally, the scalability of STS makes it practical to deploy from characterization to production with the benefit of not only optimized cost, but greatly simplified data correlation to further shorten time to market.

To ensure customer success, NI provides training, products and services supported by NI engineering professionals and NI Alliance Partners throughout the world.

For availability and configuration assistance, contact your local NI sales engineer. To learn more about the NI Semiconductor Test System, visit <a href="https://www.ni.com/sts">www.ni.com/sts</a>.

## **About National Instruments**

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